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(REVISION –	— 2015) Signature
	LOMA EXAMINATION IN ENGINEERING/TECHNOLOGY/ MANAGEMENT/COMMERCIAL PRACTICE — APRIL, 2018
	MICROPROCESSORS AND INTERFACING
	[Time: 3 hours
	(Maximum marks: 100)
	PART — A
	(Maximum marks: 10)
	Marks
I An	swer all questions in one or two sentences. Each question carries 2 marks.
1.	Name any two processors in 8086 family.
2.	Define Assembler.
3.	List any two data transfer Instruction.
4.	Expand PPI.
5.	List the operating modes of 80386.
	$(5\times2=10)$
	PART — B
	(Maximum marks: 30)
II Ar	nswer any five of the following questions. Each question carries 6 marks.
1.	Briefly describe memory segmentation in 8086 processor.
2.	List jump instructions in 8086.
3.	Write an assembly language program to display the message Welcome to ALP programming.
4.	Write any three arithmetic instructions with example.
5.	Differentiate between software interrupts and hardware interrupts.

Write short notes on MMX.

7. Discuss multi core processing.